

## CY8C4125AXQ-483

Status: In Production



CY8C4125AXQ-483	
Automotive Qualified	N
Boost Converter (V)	0
CPU Core	ARM Cortex-M0
CapSense	Y
Dedicated ADC (#Max Resolution @ Sample Rate)	SAR (1, 12-bit @ 806 ksps)
EEPROM (KB)	0
Flash (KB)	32
LCD Direct Drive	Y
Max. Operating Frequency (MHz)	24
Max. Operating Temp. (°C)	105
Max. Operating Voltage (V)	5.50
Min. Operating Temp. (°C)	-40
Min. Operating Voltage (V)	1.71
No. of CAN Controllers	0
No. of Continuous Time Blocks	1
No. of DMA Channels	0
No. of Dedicated Comparators	2
No. of Dedicated Timer/Counter/PWM Blocks	4
No. of GPIOs	36
No. of Op Amps	2
No. of SIO	0
No. of Serial Communication Blocks	2
No. of USB IO	0
No. of Universal Digital Blocks	0
SRAM (KB)	4
USB (Type)	None

PACKAGING/ORDERING	
Package	TQFP
No. of Pins	44
Package Dimensions	395 L x 1.4 H x 395 W (Mils)
Package Weight	285.67 (mgs)
Package Cross Section Drawing	<a href="#">Download</a>
Package Carrier	TRAY
Package Carrier Drawing / Orientation	<a href="#">Drawing</a> , <a href="#">Package Orientation</a>
Standard Pack Quantity	320
Minimum Order Quantity (MOQ)	320
Order Increment	320
Estimated Lead Time (days)	42
HTS Code	8542.31.0000
ECCN	(A.3)
ECCN Suball	3A991

QUALITY AND ROHS	
Moisture Sensitivity Level (MSL)	3
Peak Reflow Temp. (°C)	260 ( <a href="#">Cypress Reflow Profile</a> )
RoHS Compliant	Y <a href="#">Print RoHS Certificate of Compliance</a>
PB Free	Y
Lead/Ball Finish	Ni/Pd/Au
Marking	<a href="#">Cypress Marking Format</a>
<b>Package Material Declaration</b>	
<a href="#">44L-TQFP PB-FREE PACKAGE MATERIAL DECLARATION DATASHEET</a> <i>Last Update: Sep 07, 2015</i>	
<b>IPC 1752 Material Declaration</b>	
<a href="#">IPC-1752-2_V1.1_TQFP 44_ASET_PURE SN</a> <i>Last Update: Jul 05, 2012</i>	
<a href="#">IPC-1752-2_V1.1_TQFP 44_CML_NIPDAU_GREEN MC</a> <i>Last Update: Jul 04, 2012</i>	
<a href="#">IPC-1752-2_V1.1_TQFP44_JCET_AZ44A</a> <i>Last Update: Jul 03, 2012</i>	
<b>RoHS Analysis Certificates (CoA)</b>	
<a href="#">TSOP 28 - 001-79768</a> <i>Last Update: Jun 28, 2012</i>	
<a href="#">ZT48 - 001-79771</a> <i>Last Update: Jun 28, 2012</i>	
<a href="#">AZ32 - Q</a> <i>Last Update: Jun 28, 2012</i>	
<a href="#">AZ128 - R</a> <i>Last Update: Jun 28, 2012</i>	
<b>Device Qualification Reports</b>	
FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.	
<a href="#">QTP 123502: PSOC4A DEVICE FAMILY S8PF-10R TECHNOLOGY, FAB 4 CMI</a> <i>Last Update: Jun 26, 2015</i>	

TECHNICAL DOCUMENTS

**Application Notes (10)**

AN88619 - PSoC® 4100/4200 Hardware Design Considerations Nov 08, 2015  
AN90799 - PSoC® 4 Interrupts Oct 20, 2015  
AN73854 - PSoC® 3, PSoC 4, and PSoC 5LP Introduction to Bootloaders Sep 23, 2015  
AN92239 - Proximity Sensing with CapSense® Sep 15, 2015  
AN86233 - PSoC® 4 Low-Power Modes and Power Reduction Techniques Aug 31, 2015  
AN86439 - PSoC® 4 - Using GPIO Pins Aug 31, 2015  
AN89610 - PSoC® 4 and PSoC 5LP ARM Cortex Code Optimization Aug 31, 2015  
AN84858 - PSoC® 4 Programming Using an External Microcontroller (HSSP) Aug 26, 2015  
AN87391 - PSoC® 4 Segment LCD Direct Drive Aug 26, 2015  
AN86526 - PSoC® 4 I2C Bootloader Aug 12, 2015

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